



ALTAIR

ONLY FORWARD

Altair PolEx for Altium 2021.2

Tutorials

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Altair PolIEx for Altium Tutorial

Altair PolIEx for Altium is available for Altium users with limited functionalities. The Altair PolIEx for Altium supports limited functions of PolIEx PCB (a PCB design viewer), DFM (Design for Manufacturing), DFE (Design for Electric), SI (Signal Integrity), and Thermal analysis.

This tutorial will focus on downloading the tools needed and running a quick DFM check on a test board to confirm functionality.

For additional questions, please contact our PolIEx for Altium support team at pfasupport@altair.com.

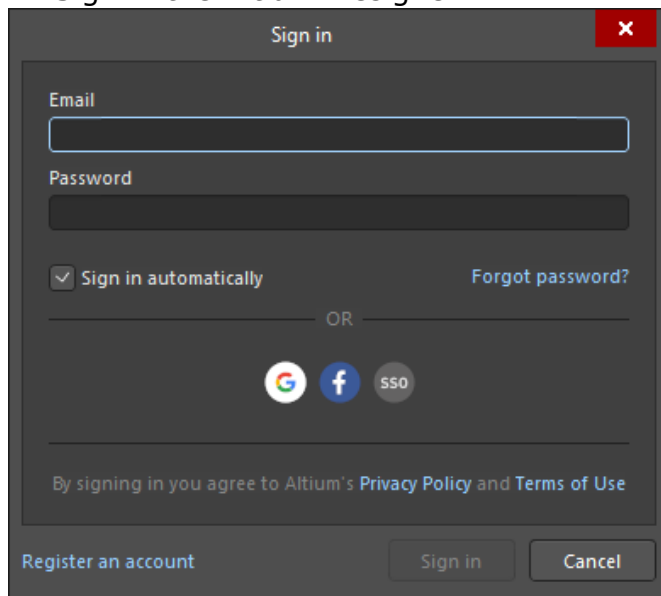
1. Altair PolIEx for Altium Workflow

The Altair PolIEx for Altium workflow has two significant parts: The PolIEx for Altium standalone software and the Altium Extension. The Altium extension will automate the process of exporting the .PcbDoc file into a PDBB project file that PolIEx can read.

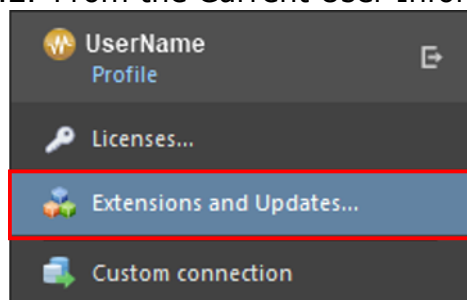
The common workflow is to use the Altair PolIEx for Altium extension to export the current state of the PcbDoc in an ASCII format. PolIEx is then used to run analysis and verification on the board. If changes to the board need to be made, they can be made within Altium Designer, and then exported again to PolIEx to run another analysis or verification.

2. Install Altium Extension

2.1. Sign in the Altium Designer



2.2. From the Current User Information, click **Extensions & Updates**.



2.3. Click **Purchased**.

2.4. Install **AltairPolIEx** extension from the Software Extensions.



2.5. Restart the Altium Designer to complete the install process.

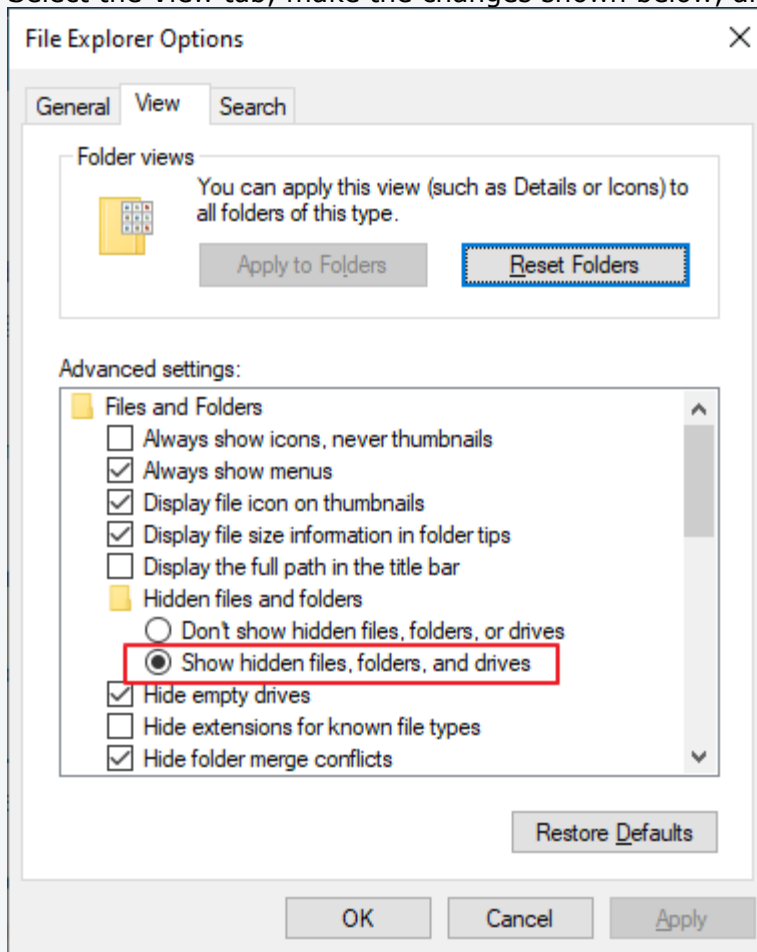
3. Altair PolIEx Environment

For this tutorial, you will use a sample file located in

C:\ProgramData\altair\PolIExForAltium\2021.2\Examples directory. This will help aid in the process of learning the Altair PolIEx tool.

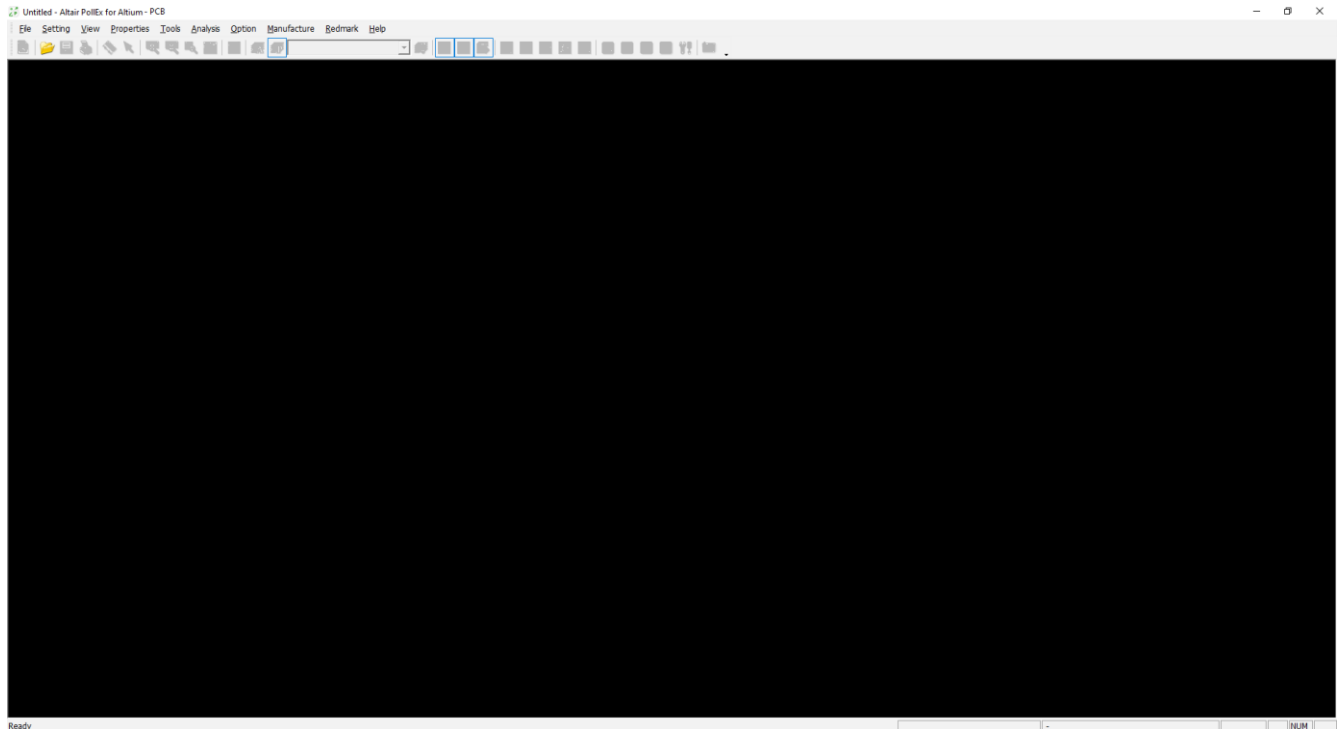
In addition to the **AltairPolIEx** for Altium Software Extension, you will use the Altair PolIEx for Altium software. You need to setup the environment the first time to use the Altair PolIEx PCB.

Note: To view the "ProgramData" folder, you will need to go to the Windows Control Panel, select Appearance and Personalization, and click Show hidden files and folders. Select the View tab, make the changes shown below, and click OK.

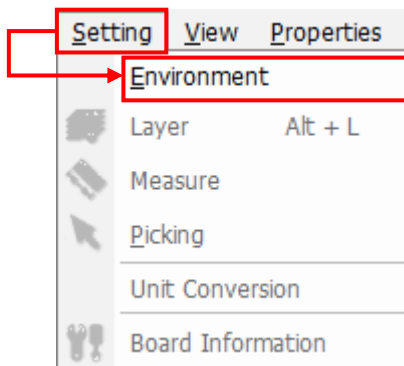


4. Altium Layer Setup in Altair PolIEx PCB

4.1. From the start menu, click **Altair 2021.2 > PolIEx PCB 2021.2**.



4.2. From the menu bar of the Altair PolIEx PCB, click **Setting > Environment**.



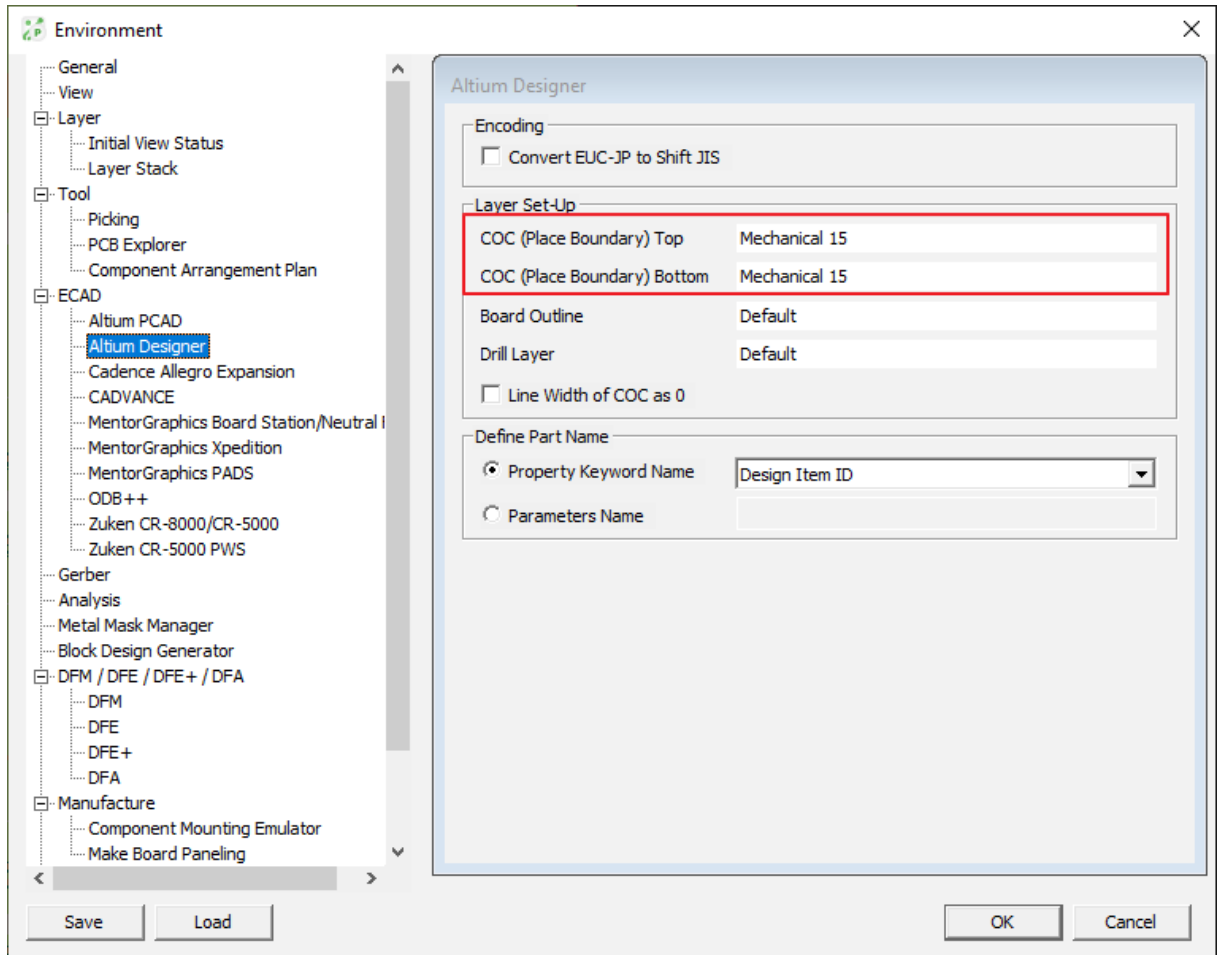
4.3. From the tree menu, click **ECAD > Altium Designer**.

4.4. Under the Layer Set-up menu, enter the following:

- For COC (Place Boundary) Top, enter `Mechanical 15`.
- For COC (Place Boundary) Bottom, enter `Mechanical 15`.



Note: The COC (Component Overlapped Check) means a component boundary.



4.5. Click **OK**.

4.6. Close Altair PolIEx PCB.



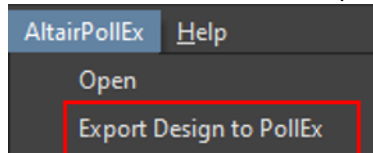
Note: The above environment setting in the Altair PolIEx PCB is essential to import correct data into Altair PolIEx. The layer names will vary depending on the Altium Designer Design Data.

5. Open Altium Designer PcbDoc Design File

- 5.1. Open the file, `AltairPollexForAltium_A01.PcbDoc`, in Altium Designer. This file is located in `C:\ProgramData\altair\PollexForAltium\2021.2\Examples` directory.
- 5.2. Click the opened PcbDoc design sample in Altium Designer and you can see **AltairPollex** extension toolbar added to the menu bar.

6. Convert PcbDoc Design File for Pollex

- 6.1. From the menu bar, click **AltairPollex > Export Design to Pollex**.



- 6.2. Altair Pollex for Altium converts the Altium Designer PCB design (*.PcbDoc) into Altair Pollex database format (*.pddb).



Note:

- Altair Pollex for Altium creates a project folder with the PCB design file name in the same directory of the Altium Designer PCB design file.
- In the created folder, **Part** folder and **PDBB** file are created.
- The Part folder is used for the Altair Pollex Solver products like SI and Thermal.

- 6.3. The converted PCB design (*.pddb) opens in **Altair Pollex PCB**.



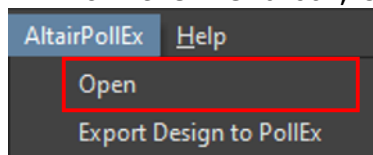
Note: If a project folder exists when you click the **Export Design to Pollex** menu, you can only save the Altair Pollex file (*.pddb).

- 6.4. Here you can either close the **Altair Pollex PCB** to go to step 8 or start evaluating Pollex using step 9.

7. Open Pollex PDBB File from Altium Designer

If a PDBB file is already created, you can easily open Pollex to view the PDBB file.

- 7.1. From the menu bar, click **AltairPollex > Open**.



- 7.2. Saved Altair Pollex design data is opened in Altair Pollex PCB.



Note: If the PDBB file is not available, you should convert PcbDoc design file into Altair Pollex file (*.pddb).

8. Run Altair Pollex DFM

8.1. From the menu bar, click **Option > DFM > DFM Input**.

8.2. Click **Load** and select `DFM_Input.DFMI` file located at `C:\ProgramData\altair\PollexForAltium\2021.2\Examples\DFM`. This is a template file for DFM, and the same can be achieved by adjusting the settings manually for each of the DFM checks you wish to run.

8.3. Click **Start Checking** to run the verification.

Pollex DFM checks whether the design violates the rules.

8.4. Check results.

The verification results display on the screen (Pass or Fail).

The screenshot displays the Altair Pollex for Altium 2021.1 - PCB software interface. The main window shows a PCB layout with various components and traces. A dialog box titled "DFM Check Item Result" is open, displaying a table of check results. The table has columns for Item, SubID, Status, and Fail. The results show that several items have failed, including PCB Outline Spacing, Component Spacing, U Name Overlap, Hole Distance, and Unrouted Net. The total fail count is 86. The software status bar at the bottom indicates "Ready" and shows coordinates (x: 59.44 y: 76.99) and units (mm, NUM).

Item	SubID	Status	Fail
PCB Outline Spacing	ALL	No Good	
Component Spacing	ALL	No Good	
U Name Overlap	ALL	No Good	
Hole Distance	ALL	No Good	
Silk On Pad	ALL	Pass	
Solder Resist pad	ALL	Pass	
Via Spacing	ALL	Pass	
Net to Net	ALL	No Good	
Pad to Net	ALL	No Good	
Unrouted Net	ALL	No Good	
Component At Reverse	ALL	No Good	
Test Point	ALL	No Good	

Total Point Result

Pass: 1892519, Warning: 0, No Good: 86

Total Limit Point: 0

Total Point: 0

Total Fail Count: 86

Total Status: No Good

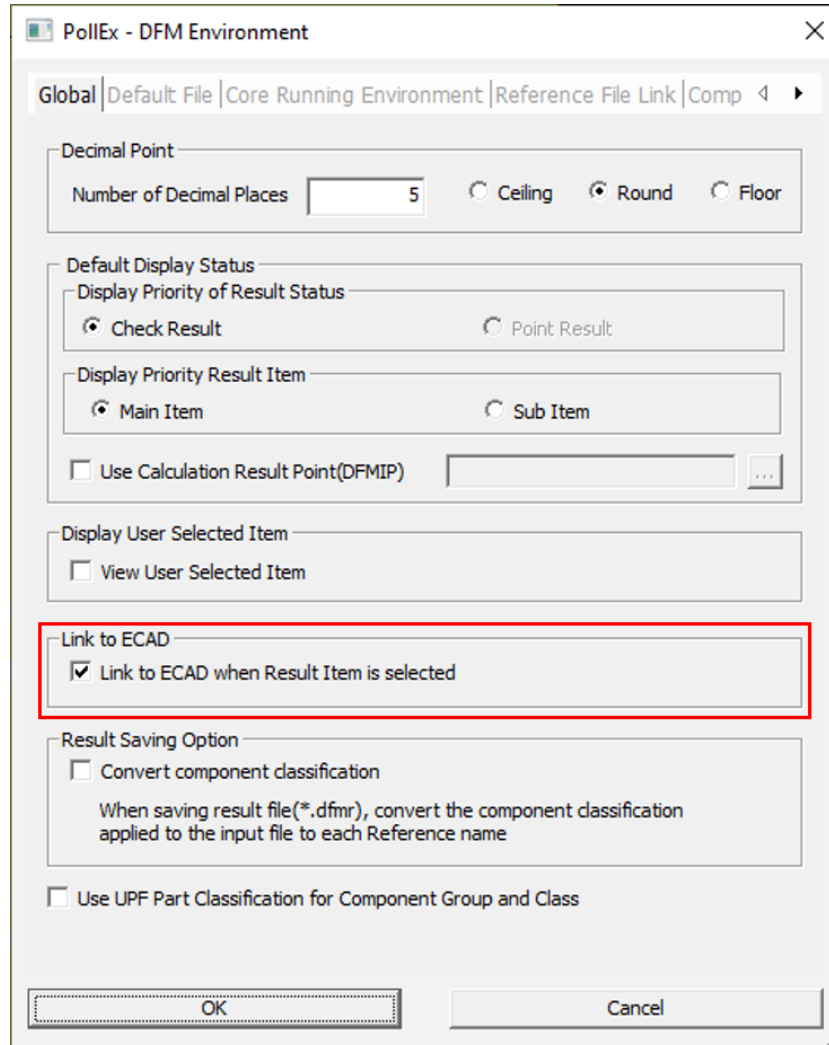
From here, you can use the DFM user guide to learn more about the settings needed to create your own rule checks. This can be found in the tutorial highlighted in the note above.

9. Link to ECAD (Cross-Probing)

When reviewing the results of the DFM checks, it might be necessary to cross probe the Altium PcbDoc files. The following steps will show you how to do that.

9.1. From the menu bar, click **Option > DFM > DFM Environment**.

9.2. Enable the checkbox of the **Link to ECAD when Result Item is selected**.



Note: If the above checkbox is enabled, you do not need to click the **Link to ECAD** button in the Altair PolIEx DFx (DFM and DFE).

9.3. Click **Ok** to close the DFM Environment dialog.

